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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the application of:

Jun-ichi KONNO et al

Serial Number: 07/743,383

ATTN: APPLICATION
BRANCH

Filed: August 21, 1991

For: METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED
CIRCUITS AND APPARATUS USED IN SUCH METHOD

RECEIVED

MAR 26 1992

REQUEST FOR CORRECTED FILING RECEIPT

APPLICATION DIVISION-400

Honorable Commissioner
Washington, D.C. 20231

March 23, 1992

Sir:

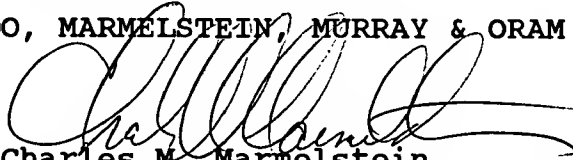
Please supply the undersigned attorney with a corrected filing receipt of the above-identified application.

In reviewing the Official Filing Receipt, we noted an error in the fifth inventor's name. The fifth inventor's name should read -- TETSUO KONDO --. In support of this correction, we are enclosing a copy of the Declaration and Power of Attorney filed with the above-identified application, and a copy of the filing receipt with the correction highlighted in yellow.

In the event any fees are due with respect to this paper, please charge our Deposit Account No. 14-1060.

Respectfully submitted,

NIKAIDO, MARMELSTEIN, MURRAY & ORAM


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CMM:mms

Enclosure: Declaration and Power of Attorney (copy)
Official Filing Receipt (copy)

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SERIAL NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CL	IND CL
07/743,383	08/21/91	1304	\$ 560.00	911333	7	22	4

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Receipt is acknowledged of the patent application identified herein. It will be considered in its order and you will be notified as to the examination thereof. Be sure to give the U.S. SERIAL NUMBER, DATE OF FILING, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this transmittal.

Applicant(s)

JUN-ICHI KONNO, MIE, JAPAN; KEISUKE SHINAGAWA, KANAGAWA, JAPAN; TOSHIYUKI ISHIDA, KANAGAWA, JAPAN; TAKAHIRO ITO, KANAGAWA, JAPAN; ~~TETSUO KONDO~~, KANAGAWA, JAPAN; FUKASHI HARADA, MIE, JAPAN; SHUZO FUJIMURA, TOKYO, JAPAN.

TETSUO KONDO

FOREIGN/PCT APPLICATIONS-PCT
JAPAN

PCT/JP91/00861 06/26/91
2-171791 06/27/90

TITLE
METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED CIRCUITS AND APPARATUS
USED IN SUCH METHOD

PRELIMINARY CLASS: 156

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